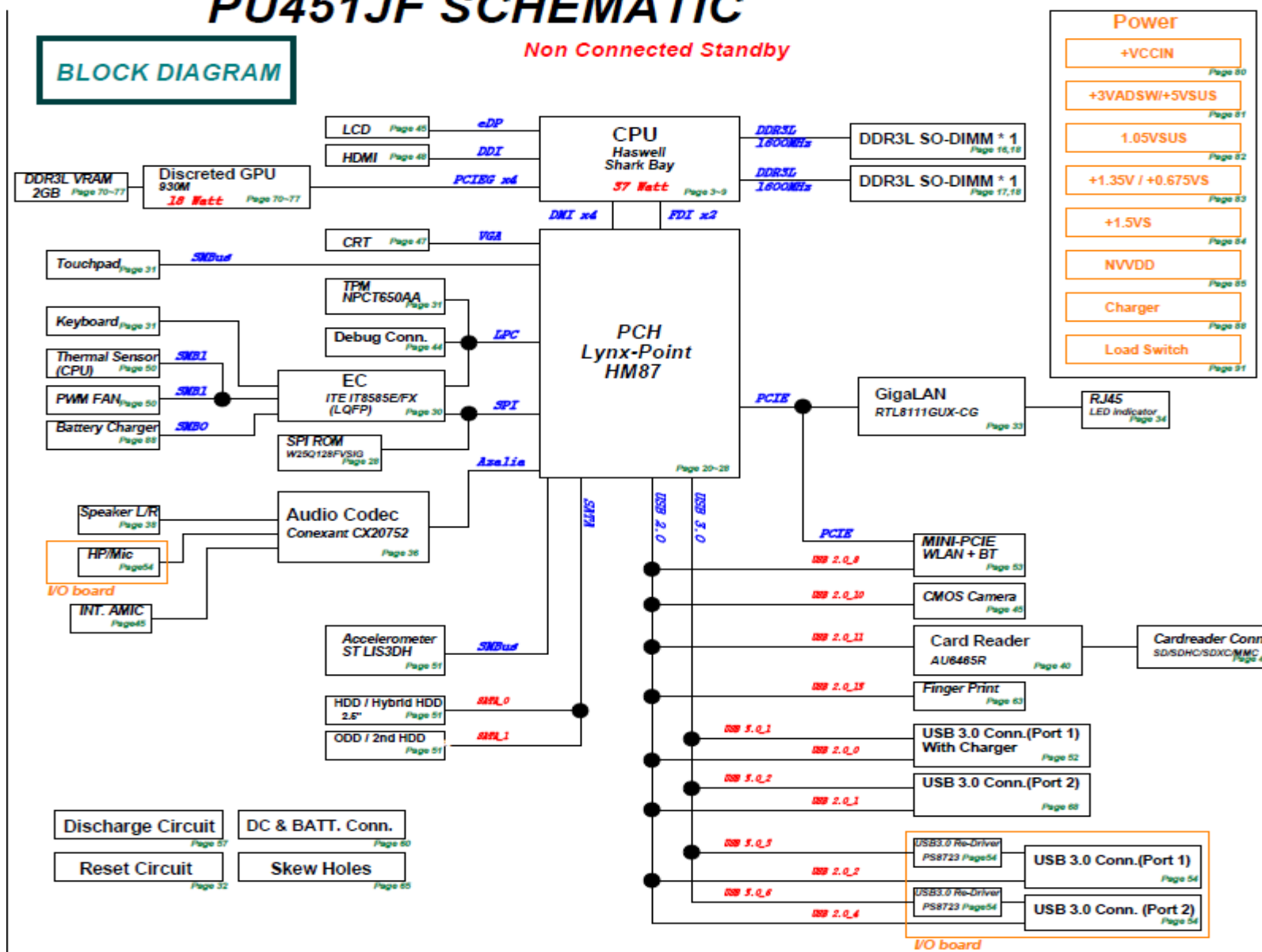


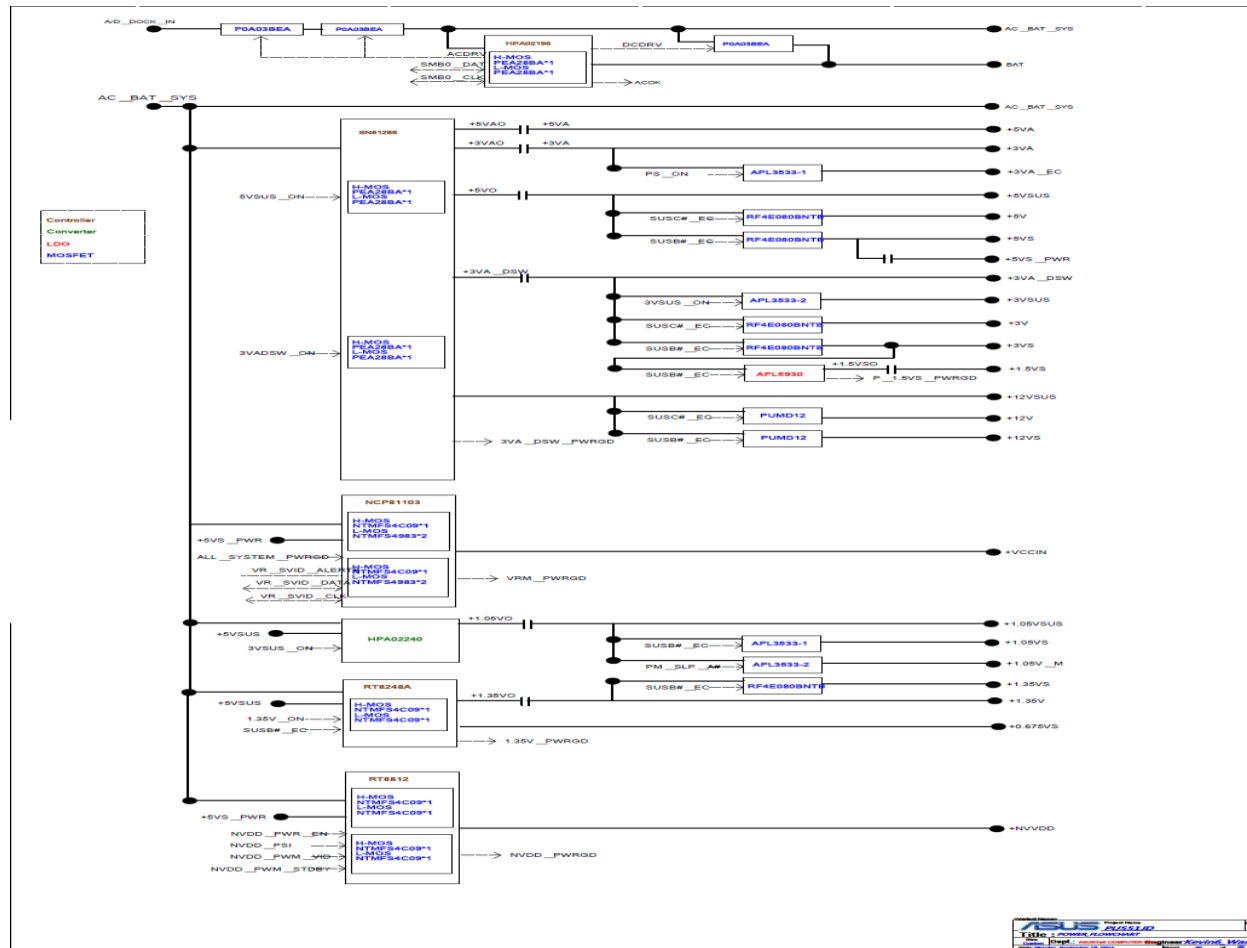
PU451JF Repair Guide

BLOCK DIAGRAM

PU451JF SCHEMATIC

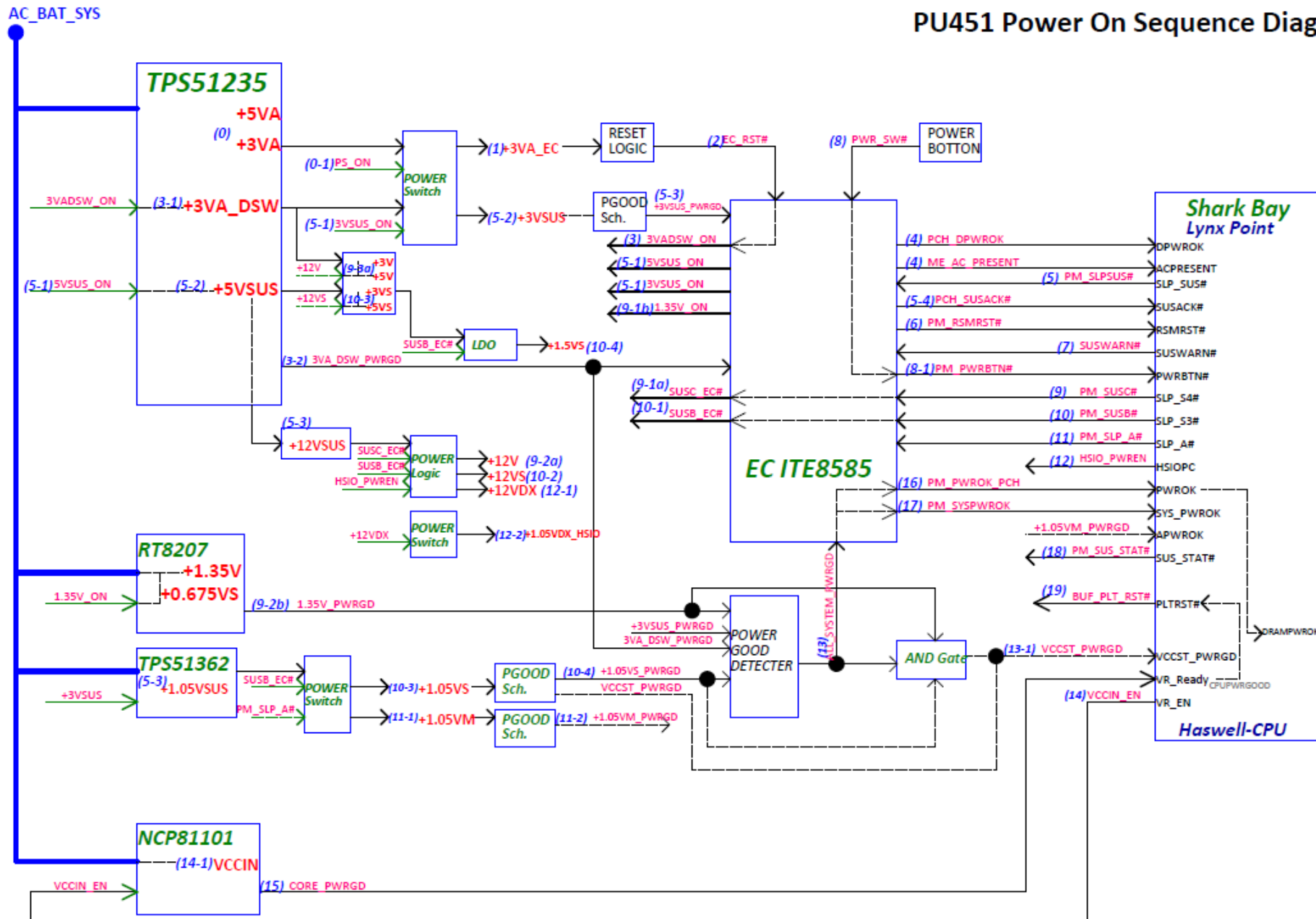


POWER FLOW



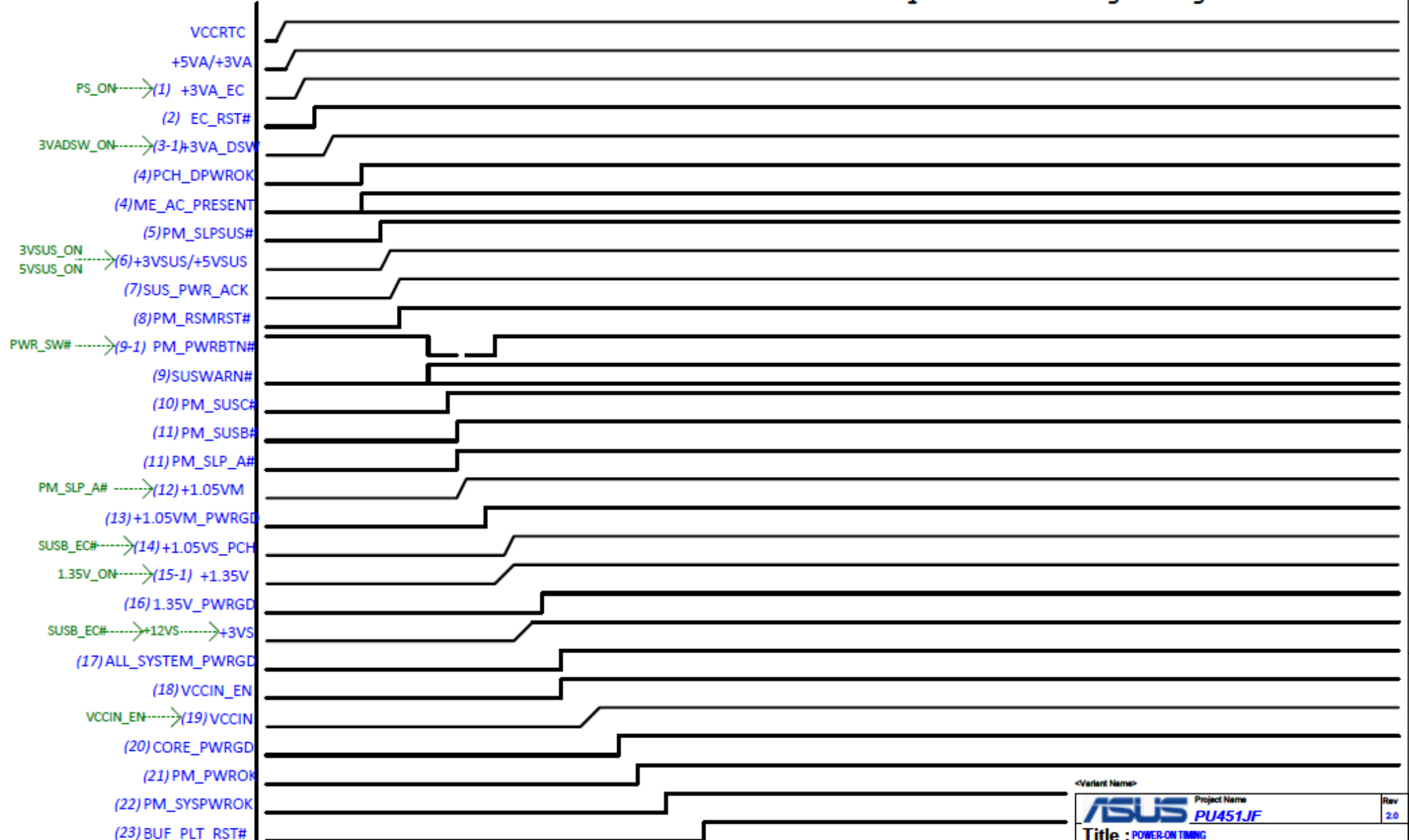
POWER ON SEQUENCE

PU451 Power On Sequence Diagram Rev.1.0



AC POWER ON SEQUENCE

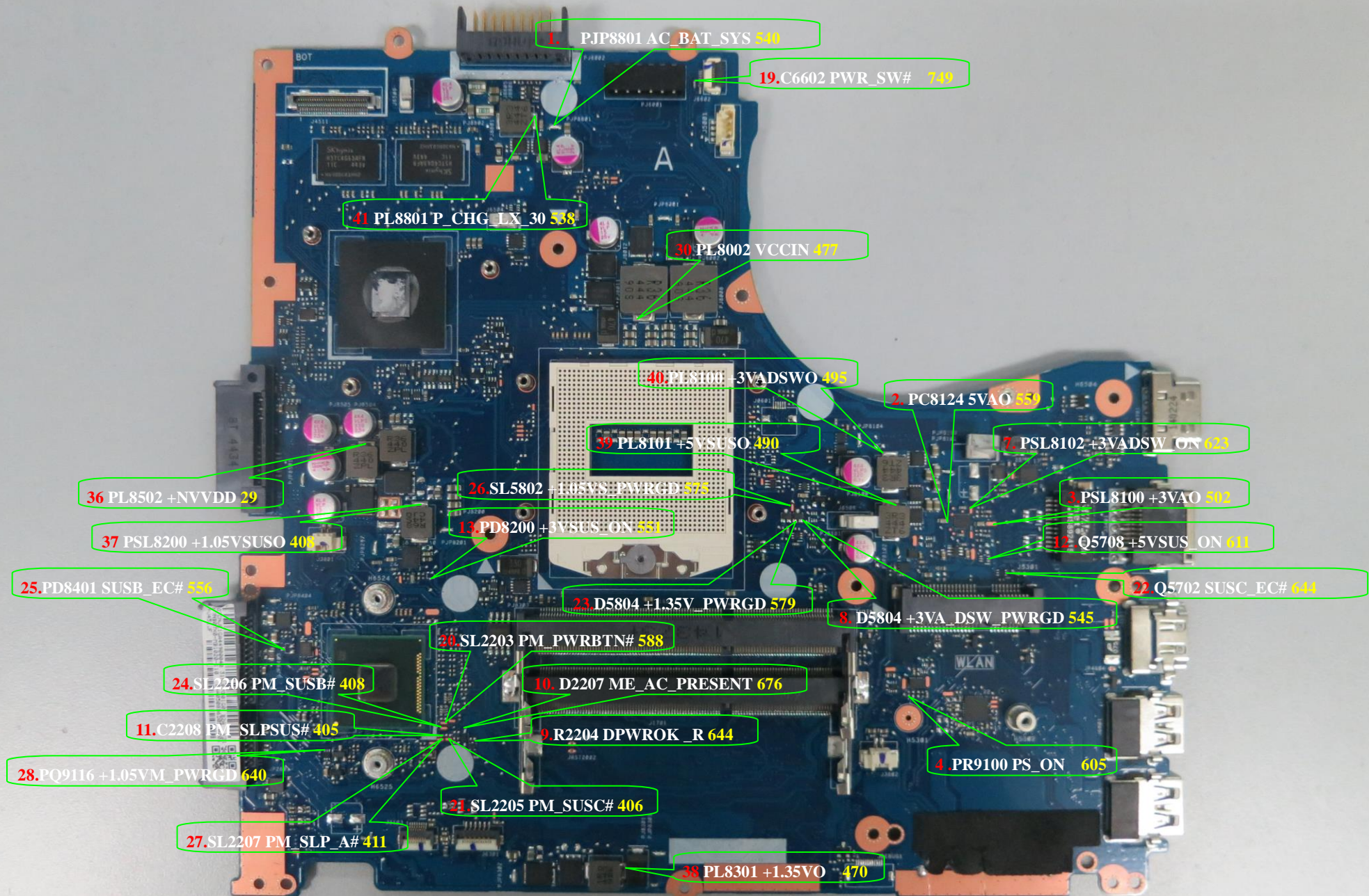
BU401 Power-On Sequence Timing Diagram Rev.0.1



<Variant Name>

Project Name		Rev
ASUS PU451JF		2.0
Title : POWER-ON TIMING		
Size	Dept.: ASUS*OK COMPUTER	Engineer: Jeff Kao
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Signal Measure Point-Bottom



Signal Measure Point-Top

